

JC474 U.S. PTO

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07/26/02

PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10064561	FILING DATE 07/26/2002	CLASS 438	SUBCLASS 300	GAU 2812	EXAMINER Lindsay
<b>**APPLICANTS:</b> Chang Kent;					
<b>**CONTINUING DATA VERIFIED:</b> None will					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> TAIWAN 91114489 07/01/2002 will					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials will				ATTORNEY DOCKET NO 9167-US-PA	
TITLE : Method for fabricating raised source/drain of semiconductor device U.S. DEPT. OF COMM /PAT. & TM-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>			<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
		<b>Primary Examiner</b>	Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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